REMARKS

Claims 1-7 are pending in this application. Claim 7 has been added.

The specification has been amended to correct headings, insert a cross-reference to the Applicants' Japanese priority application and editorially place the specification in compliance with U. S. practice.

Additionally, a new Abstract has been inserted which deletes the reference numerals and places the Abstract in compliance with U. S. practice.

Claims 1-6 have been editorially amended to place them in compliance with U.S. practice. Additionally, new claim 7 has been added.

Conclusion

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact Daniel K. Dorsey (Reg. No. 32,520) at the telephone number of the undersigned below, to conduct an interview in an effort to expedite prosecution in connection with the present application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By _____ #19,382
Terrell C. Birch, #19,382

P.O. Box 747
Falls Church, VA 22040-0747
(703) 205-8000

TCB/DKD/slb
0951-0130P
DKD

Attachment(s): Abstract of the Disclosure

ABSTRACT OF THE DISCLOSURE

A receiving apparatus may be provided with a variable gain amplifier amplifying a received signal, an output level of which is detected by a level detection circuit. A comparing circuit compares an output from a level detection circuit to a reference level. There is a binarizing circuit and a detection circuit for detecting a switching of a gain of a variable gain amplifier. A slice level holding circuit holds, at substantially constant value, a slice level employed at the binarizing circuit. A counter circuit and at least one of the slice levels may be held at substantially constant value for a prescribed time when switching of the gain of a variable gain amplifier is detected, rendering at least partially ineffective any noise produced during switching of the gain of the variable gain amplifier.